

FIG. 1 (Prior Art)

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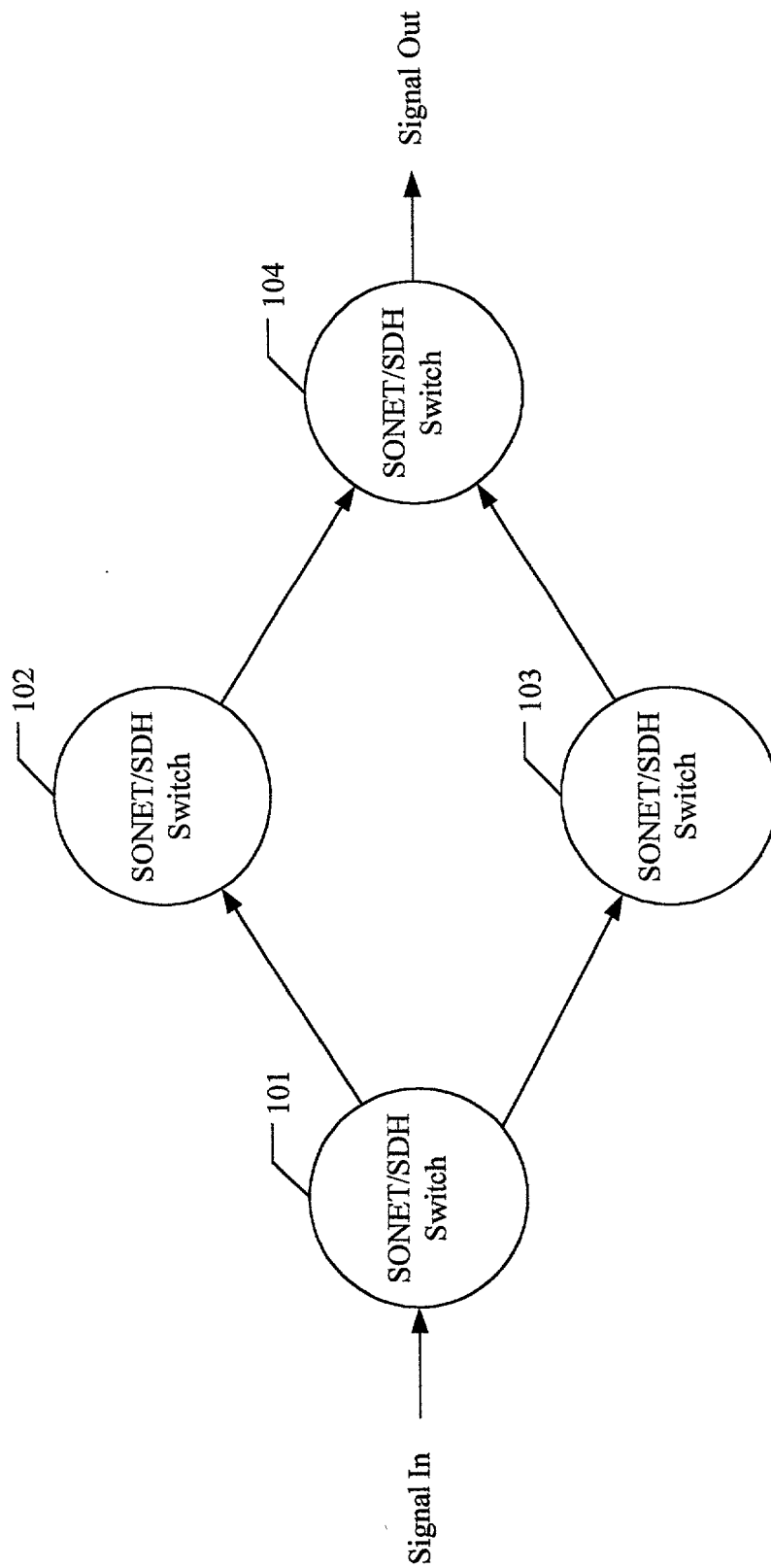


FIG. 2

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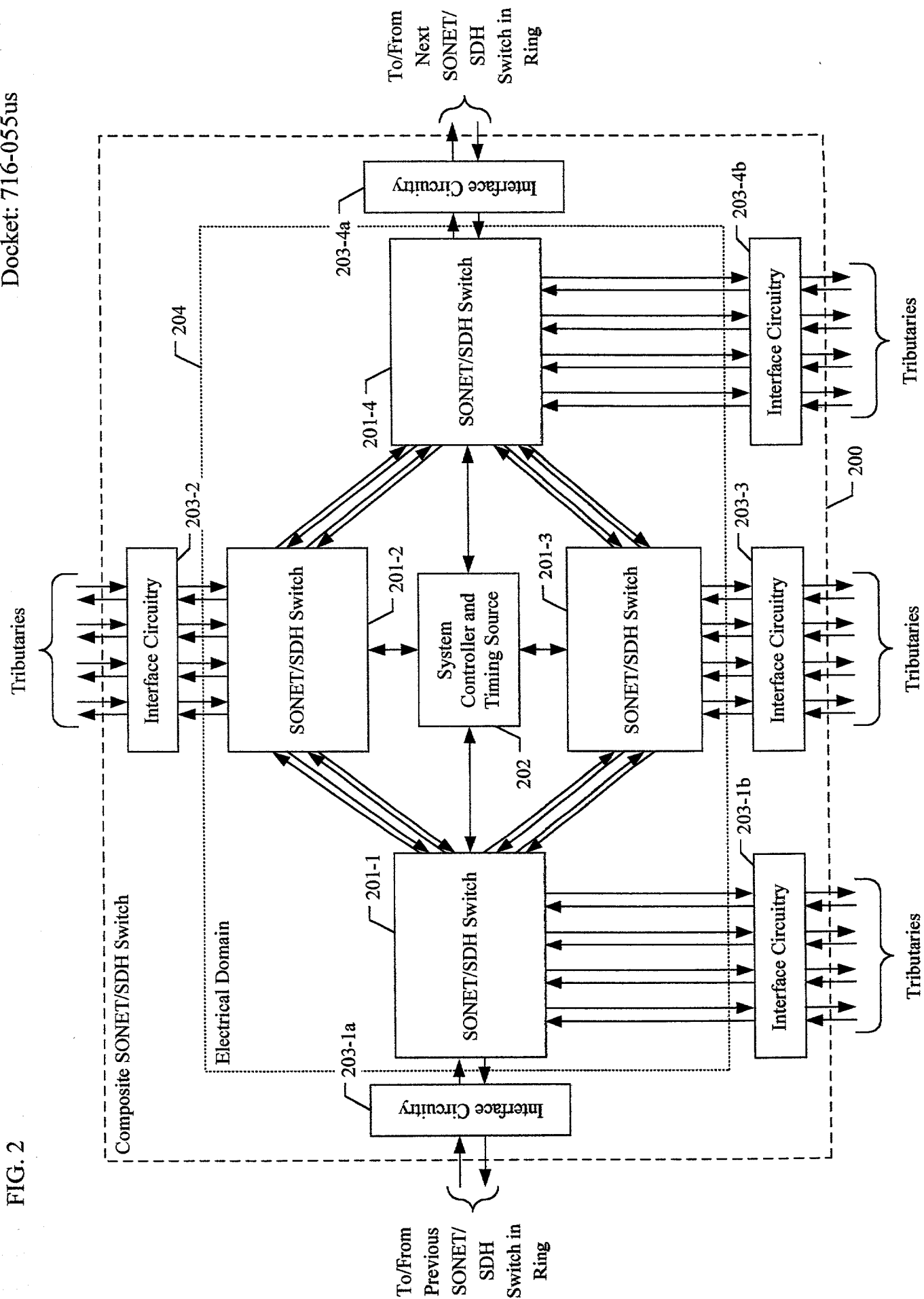


FIG. 3

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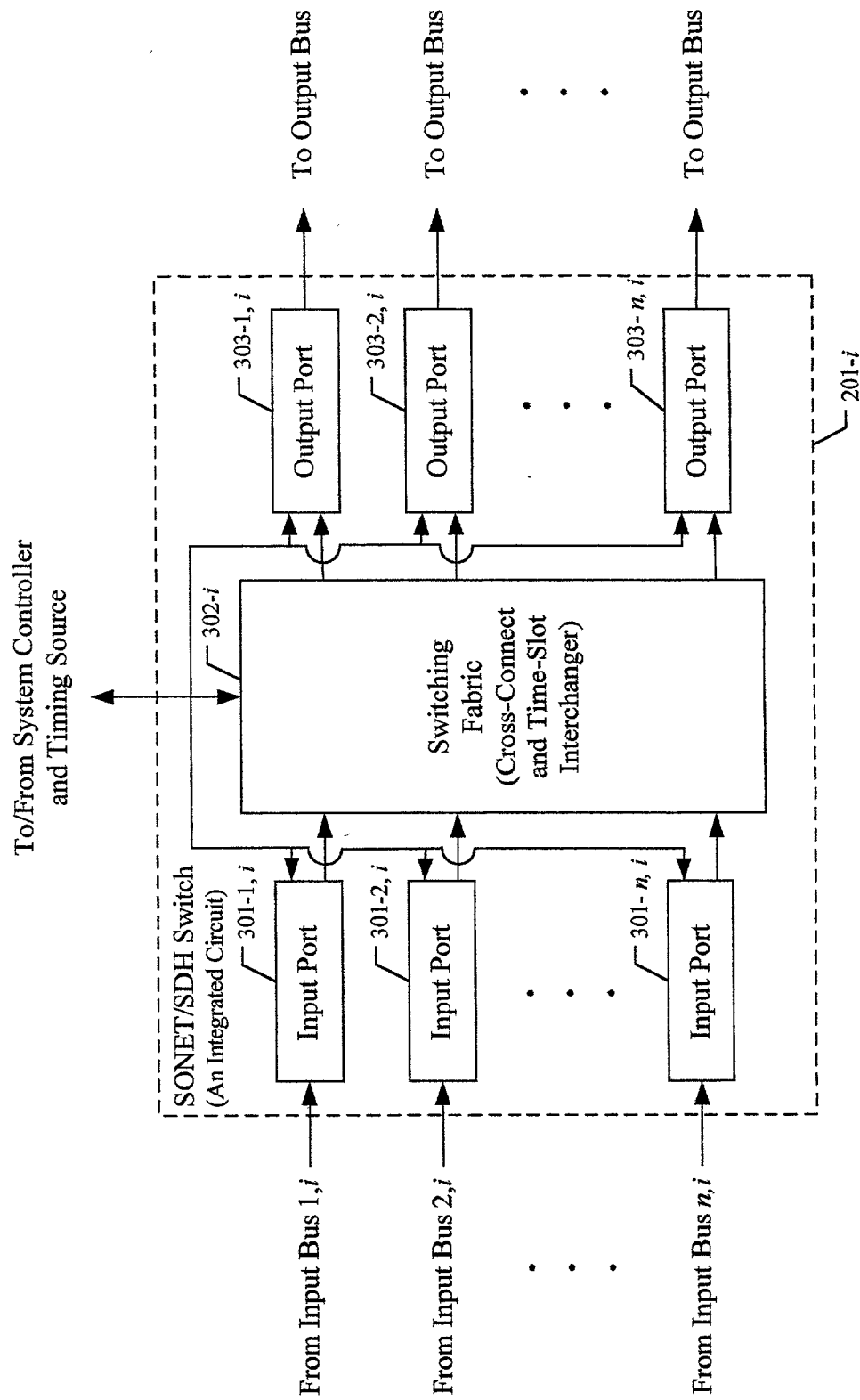


FIG. 4

Value of Read Pointer, Frame Synchronization
Signal, and System Clock from System Controller
202; Contents of Frame Position Register 54- j,i to
System Controller 202

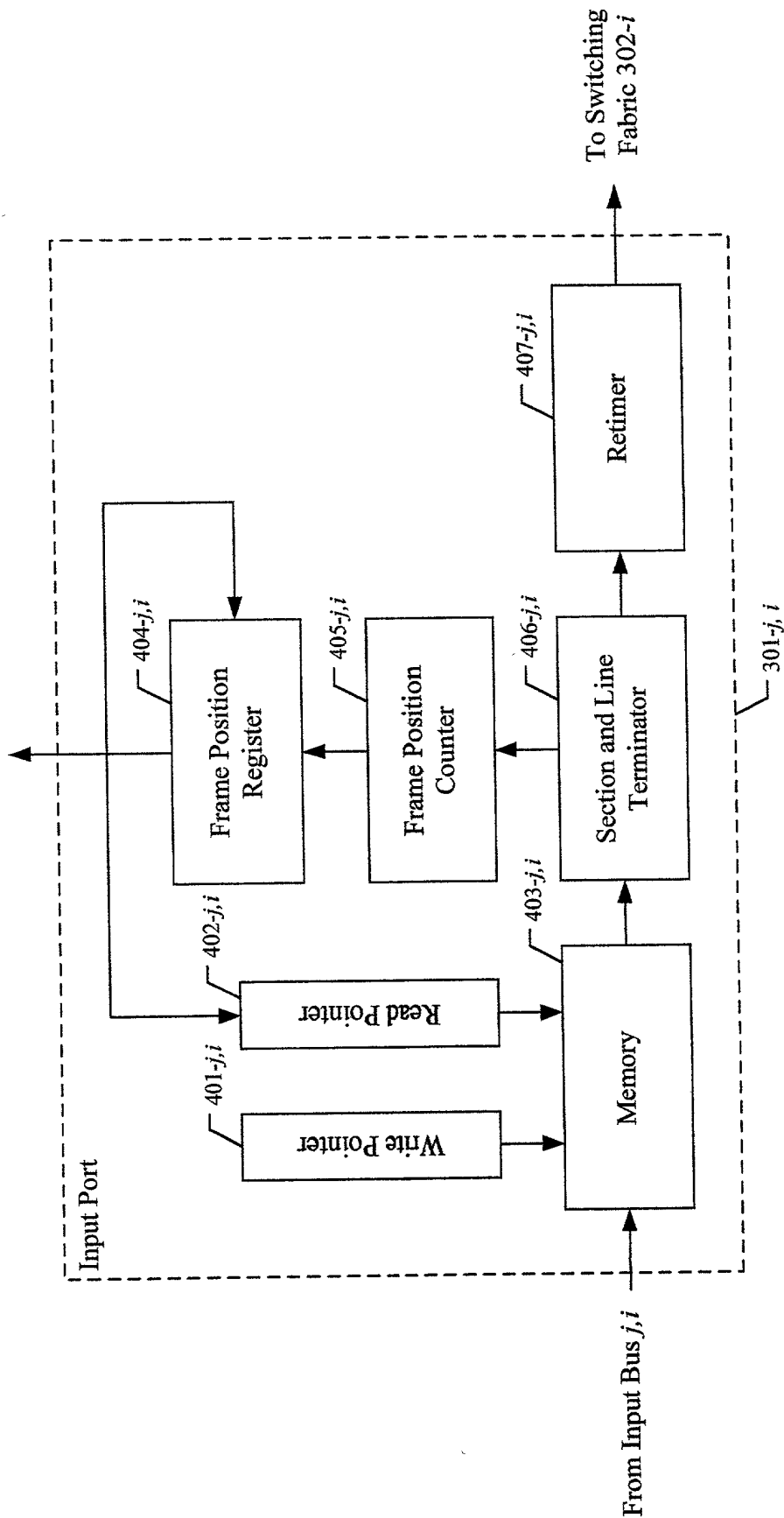


FIG. 5

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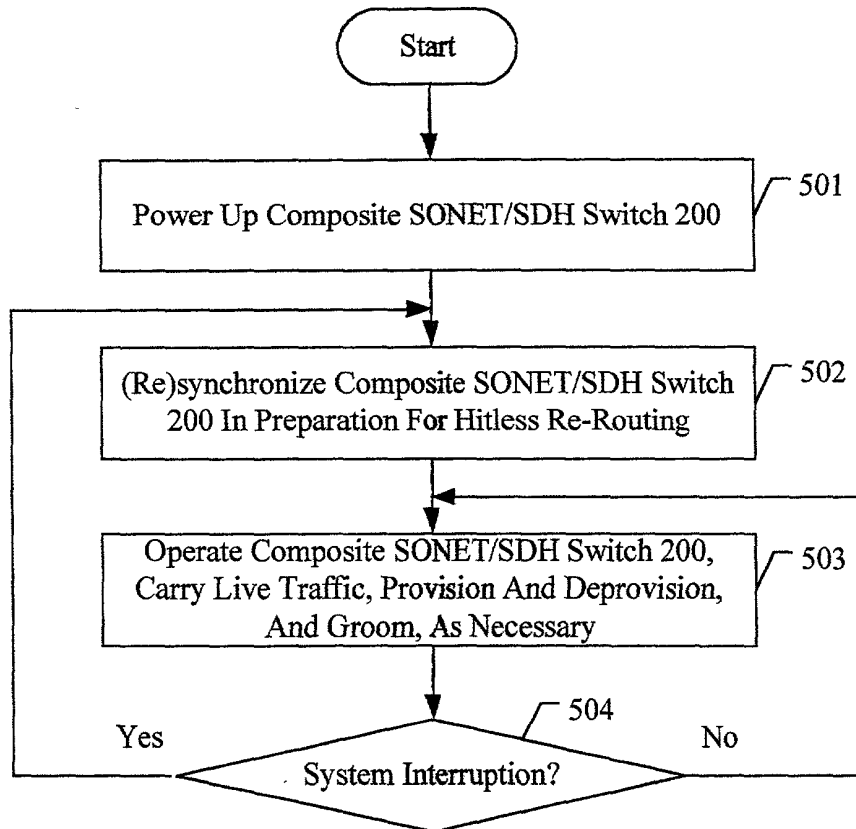


FIG. 6

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